

CACHE UNIT OR NONVOLATILE UNIT DATA_BUS (0-3,0-9) SYNCOUNT (A:B) SYNCIN (A:B)-ALIVE (0-1) -CMD / # DT ---HWCHK: (0:-1) DTOUT / * DTIN SEL (0-1) -UNIT CHANNEL OR CONTROL UNIT

F16.2

F1G. 3

DATA BUS PROTOCOL

| | COMMAND | DRESS WORD | -count chk- | CODE, DATA, | DATA, | $\chi = \chi_{c}$ | CHK-CODE |
|---|---------|------------|-------------|-------------|----------|-------------------|----------|
| 1 | | / \ | / \ | /\/ | <u> </u> | /\/\ | /\ |

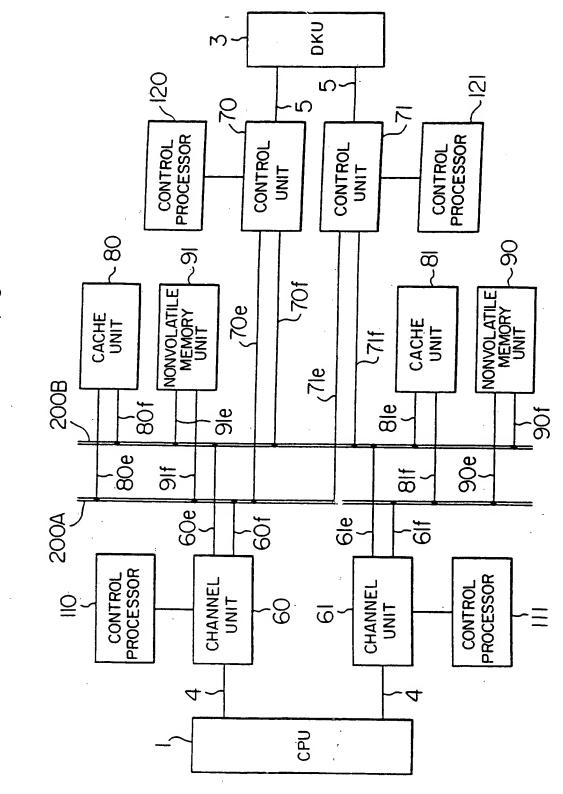
FIG. 4

DATA BUS MODE

| DUOUT/*DTIN | CMD/*DT | BUS MODE |
|-------------|----------|------------------------|
| 1 | l | TRANSFER COMMAND |
| 1 | 0 | TRANSFER WRITE DATA |
| 0 | ! | TRANSFER STATUS |
| 0 | 0 | TRANSFER READ DATA |

DKO 2 2 PROCESSOR CONTROL CONTROL CONTROL CONTROL LIND UNIT 2 121 あく 7 0 0 816× **PQ** 710 NONVOLATILE MEMORY UNIT **d**Q2 **2**90 ~90B 9IA~ 71b NONVOLATILE MEMORY UNIT 90A~ 200 6 0 b ~80B ele ele CACHE ~88 CACHE 80A~ 80~ 9 elc **BIA**~ **60**0 909 6la pl9 $\frac{8}{1}$ CONTROL PROCESSOR CONTROL PROCESSOR CHANNEL UNIT CHANNEL 0 UNIT 9 4 4 CPU

F16.5



F1G. 6

